

CURRICULUM VITAE

Dr. Nejmeddine BAHRI

FEBRUARY 2020

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PERSONAL INFORMATIONS

Bahri Nejmeddine



Gender: Male | Date of Birth: 10/12/1985 | Nationality: Tunisian

Marital Status: Married

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PROFESSION	Assistant Professor
CURRENT APPOINTMENTS:	<ul style="list-style-type: none"> ▪ Assistant Professor in Electronics and Embedded systems at the Faculty of Sciences of Monastir (FSM). ▪ Researcher Member in the Laboratory of Computer Sciences LIGM Gaspard Monge at ESIEE Paris. ▪ Researcher Member in the Laboratory of Electronics and Information Technology at National Sfax School of Engineers. ▪ President of Robotics CLUB FSM-ROBOTECH at FSM ▪ Member of the Junior International Chamber (JCI) ▪ Member of Tunisian Red Crescent

PROFESSIONAL EXPERIENCES

Year	Function
From 2015 to 2020	Assistant Professor at the Faculty of Science of Monastir (FSM) (Tunisia)
From 2016 to 2020	Temporary Assistant Professor at polytechnic engineering school of Sousse (Tunisia)
2017/2018	Temporary Assistant Professor at National School of Engineers of Monastir (ENIM) (Tunisia)
From 2010 to 2015	Temporary Assistant Professor at National School of Electronics and Telecommunications of Sfax (ENET'COM) (Tunisia)

LIST OF ACADEMIC DEGREES

Year	Institution	Diploma	Mention
2010 - 2015	High School of Engineers in Electronics and Electrotechnics (ESIEE Paris, Paris-EST University)	PhD in Computer Science	Highest Honors
2010 - 2015	National School of Engineers of Sfax (ENIS)	PhD in Electrical Engineering	Highest Honors
2009 - 2010	National School of Engineers of Sfax (ENIS)	Master Degree in Electronics	Good
2006 - 2009	National School of Engineers of Sfax (ENIS)	Electrical Engineering Diploma	Very Good
2004 - 2006	Preparatory Institute of Engineering Studies of Monastir (IPEIM)	First cycle diploma	National Entrance Exam to Engineering Schools
2003 / 2004	Secondary High School of EL JEM- Mahdia Tunisia	National Baccalaureate in Experimental Sciences	Good

AREAS OF EXPERTISE

My research interests and teaching expertise cover the following topics:

- Image and video processing
- IOT (Internet Of Things)
- Embedded Systems (ARM, FPGA, DSP, Microcontrollers)
- Embedded Linux
- Parallel implementation on multicore Architectures: Multi-task, Multi-thread: Pthread, OpenMP
- Home automation (Domotics)
- Video surveillance

LANGUAGES AND IT SKILLS

Spoken languages	<ul style="list-style-type: none"> ▪ Arabic, French, and English
IT Skills	<ul style="list-style-type: none"> ▪ Programming Languages: C/C++, Embedded C, Python, VHDL, Matlab, Labview, Assembler ▪ Operating Systems: Windows, Linux, ANDROID ▪ Embedded Operating Systems: (SYS BIOS, μClinux, Raspbian) ▪ Parallel Programming : Multi-task, Multi-thread, Multi-core: Pthread, OpenMP ▪ Office: Microsoft Office, Libre Office

PEDAGOGICAL ACTIVITIES

- Teaching at the Faculty of Sciences of Monastir (FSM, University of Monastir) since September 2015.
- Teaching at Polytechnic Engineering School of Sousse since 2016.
- Teaching at ENET'Com (National School of Electronics and Telecommunications of Sfax) from 2010 to 2015.
- Head of Practical Activities Laboratory (Analog Electronics) within the Department of Physics at the FSM since 2015.
- Co-supervision of PhD Thesis at the University of Sfax in collaboration with Médéa Algeria University : (1 Thesis achieved: Dr. Mohamed Maazouz).
- Co-supervision of PhD Thesis at the University of Sfax in collaboration with Fez Morocco University : (1 Thesis achieved: Dr. Abdessamad Alansari).
- Supervision of end-of-studies projects and professional Master projects at the University of Sfax and University of Monastir 2011-2018: (26 projects (Engineers + License), 9 Professional Masters).
- Organizing committee Member of the Scientific Day in Electronics & New Technologies JSENT at the Faculty of Sciences of Monastir: 2015/2016 and 2016/2017.
- Scientific committee Member of the conference SETIT'16 and SETIT'18.
- Member of the CMCU PHC-Utique and CMCU PHC-MAGREB cooperation program.
- President of the FSM-RBOTECH Robotics and Technologies CLUB at the FSM 2016-2017, 2017-2018 and 2018-2019.
- A Jury Member of the 5th and 6th edition of the Science Festival (Monastir Science Palace: April 2018 and 2019).

PEDAGOGICAL SUPPORTS

Support	Institution / Year
Embedded C/C++ and Multicore DSP programming (Digital Signal Processor)	<ul style="list-style-type: none"> ◆ Polytechnic engineering School of Sousse / 2018 ◆ ENET'Com Sfax / 2013 to 2015
Algorithmic and C/C++ Programming	<ul style="list-style-type: none"> ◆ Polytechnic engineering School of Sousse / 2018 and 2020 ◆ ENET'Com Sfax / 2011 to 2015
Industrial Computer Sciences	<ul style="list-style-type: none"> ◆ Faculty of Sciences of Monastir (FSM) / 2016, 2017, 2018, and 2019
Advanced programming	<ul style="list-style-type: none"> ◆ Faculty of Sciences of Monastir (FSM) / 2016, 2017 and 2018
Embedded systems programming	<ul style="list-style-type: none"> ◆ Faculty of Sciences of Monastir (FSM) / 2016, 2017, 2018 and 2019
Assembler language programming	<ul style="list-style-type: none"> ◆ Faculty of Sciences of Monastir (FSM) / 2016, 2017, 2018 and 2019
Image and audio processing	<ul style="list-style-type: none"> ◆ Faculty of Sciences of Monastir (FSM) / 2017 and 2018
Analog electronics	<ul style="list-style-type: none"> ◆ Faculty of Sciences of Monastir (FSM) / 2016 and 2017
Microprocessors and Microcontrollers	<ul style="list-style-type: none"> ◆ Polytechnic engineering School of Sousse / 2017, 2019, and 2020 ◆ Faculty of Sciences of Monastir (FSM) / 2016, 2017, and 2018 ◆ ENET'Com Sfax / 2011 to 2015
Embedded Linux and Embedded Systems	<ul style="list-style-type: none"> ◆ Polytechnic engineering School of Sousse / 2017 to 2020
Arduino Training	<ul style="list-style-type: none"> ◆ National School of Engineers of Monastir (ENIM) / 2018
Electrical Circuits	<ul style="list-style-type: none"> ◆ ENET'Com Sfax / 2011 to 2015

LIST OF SUPERVISED (END-OF-STUDIES PROJECTS)

N°	Title	Level	Institution	Year
1	Design of an on-board test and control system for automotive antennas	GEA	Polytechnic - Sousse	2018/2019
2	Analysis of foot movements using an IMU sensor	GEA	Polytechnic - Sousse	2018/2019
3	Development of an IOT Module integrating Wi-Fi / Bluetooth and IR and Creation of an interface to control connected home automation objects	Master Pro Sys Embarqués	Polytechnic - Sousse	2018/2019
4	Design of a real-time geolocation system for public transport based on LoRa GPS	Master Pro Sys Embarqués	Polytechnic - Sousse	2018/2019
5	Development of an autonomous all-terrain mobile robot for load transport	Master Pro Sys Embarqués	Polytechnic - Sousse	2018/2019
6	Design, development and implementation of a LORA gateway with integration of a PSQL database for an on-board "smart Grid" system	3 ING	Polytechnic - Sousse	2017/2018
7	Development of a digital data transmission and encryption system	Master Pro	FSM - Monastir	2017/2018
8	Development of an IOT system: application for home automation	LFEEA3	FSM - Monastir	2017/2018
9	Design and production of an automated incubator	LFEEA3	FSM - Monastir	2017/2018
10	Design and development of a mini CNC	LFEEA3	FSM - Monastir	2017/2018

11	Design of a dashboard for bikes	LFEEA3	FSM - Monastir	2017/2018
12	Design of a comfort-line automotive system based on Raspberry Pi	Master Pro	FSM - Monastir	2016/2017
13	Remote monitoring and control system for the climatic conditions of an agricultural greenhouse	LAP3	FSM - Monastir	2016/2017
14	Design and realization of a digital system for measuring the surface of objects by camera	LAP3	FSM - Monastir	2016/2017
15	Design and implementation of a reading aid system for the blind	LFEEA3	FSM - Monastir	2016/2017
16	Design of a remote control system for an agricultural greenhouse via the web	LFEEA3	F FSM - Monastir	2016/2017
17	Design and construction of a rescue boat	LFEEA3	FSM - Monastir	2016/2017
18	Realization of a sleep detection system while driving	LFEEA3	FSM - Monastir	2016/2017
19	Creation of a remote-controlled exploration robot via the web	LFEEA3	FSM - Monastir	2016/2017
20	Creation of a safe secure	LFEEA3	FSM - Monastir	2016/2017
21	Realization of a smart home controlled by voice via an Android application	LFEEA3	FSM - Monastir	2015/2016
22	Design and production of an exploration robot controlled by Arduino via a web application	LFEEA3	FSM - Monastir	2015/2016
23	Conception and realization of a color sorting station controlled by Arduino	LFEEA3	FSM - Monastir	2015/2016
24	Design of a home automation security system based on a GSM module	LFEEA3	FSM - Monastir	2015/2016
25	Design of a line following robot	LFEEA3	FSM - Monastir	2015/2016
26	Gant-controlled robot based on a gyro-accelerometer	LFEEA3	FSM - Monastir	2015/2016
27	Design of a home automation control system via WIFI	Master Pro	ENET'Com - Sfax	2014/2015
28	Design and realization of an intelligent presence detection system based on pyroelectricity	Master Pro	ENET'Com - Sfax	2014/2015
29	Optimization of the intra prediction module for the HEVC video encoder	Master Pro	ENET'Com - Sfax	2014/2015
30	Study of broadcasting and design of an audio frequency distributor	Master Pro	ENET'Com - Sfax	2014/2015
31	Access control management system based on a Smartphone and Bluetooth communication	TRT3	ENET'Com - Sfax	2014/2015
32	Design and production of a line follower robot based on a Freescale platform	3 ING GEC	ENET'Com - Sfax	2014/2015
33	Creation of an audiovisual call to prayer system	TET3	ENET'Com - Sfax	2014/2015
34	Design of an access control system based on an RFID module	TET3	ENET'Com - Sfax	2011/2012
35	Design and production of an industrial process control system based on PICs	T2I3	ENET'Com - Sfax	2011/2012

PEDAGOGICAL TRAININGS &
WORKSHOPS

Training	Institution	Year
Deep Learning	ENIS - Sfax	2018
JAVA Programming & Applications	FSM - Monastir	2018
Civil Electricity et AutoCAD	Monastir Polyelectronic Training Center	2017
Pedagogical training on teaching in higher education	Monastir Science Palace	2016 and 2017
Scientific day in electronics and new technologies	FSM - Monastir	2016 and 2017
Educational Model 06-2014 pour TIA PORTAL	FSM - Monastir	2016
Training on "Technology of CMS components"	FSM - Monastir	2016
English engraving technology (Techno Drill 3D)	FSM - Monastir	2016
Basic First Aid (SOS) training	SOS Training Center	2016
JCI IMPACT	Achouria Primary School - EL JEM	2016
Cloud Computing	ISGI - Sfax	2015
Mentor Graphics tools for Microelectronics and embedded systems	ST Microelectronics Rabat-Morocco	2014
Linux Operating Systems : UBUNTU	ISBS - Sfax	2014
English oral communication skills	ENIS - Sfax	2013
English for academic purposes : «Speaking in academic setting»	ENIS - Sfax	2012
English for academic purposes : «Listening in academic setting»	ENIS - Sfax	2012
English for academic purposes : «Writing research articles»	ENIS - Sfax	2012
Writing Documents using LATEX	ISBS - Sfax	2011
implementation on general multiprocessor supports	Monastir Workshop AMINA 2010	2010
English for academic settings	ENIS - Sfax	2010
Spring School of Pedagogy	ENIS - Sfax	12-13 June 2010
Educational training cycle	ENIS - Sfax	January 2010



ACADEMIC
RESEARCH ACTIVITIES

Topic of research	Implementation of video coding algorithms (H264 / AVC and HEVC) on parallel architectures
Research structures	<ul style="list-style-type: none"> ◆ Researcher in the Laboratory of Computer Sciences LIGM Gaspard Monge at ESIEE Paris. ◆ Researcher in the Laboratory of Electronics and Information Technology at National School of Engineers of Sfax.
Published Journal articles	12
Published communications	14
Book Chapters	2
R&D projects	Cooperation project between Tunisia and France: CMCU PHC-Utique (2011-2015)
Scientific events	Cooperation project between Tunisia and Morocco: CMCU PHC-Maghreb (2014-2018)

RESEARCH INTERNSHIPS

Year	Internship / Collaboration
From March 2014 until now	<ul style="list-style-type: none"> ◆ Scientific collaboration with the Faculty of Sciences and Techniques FST (Fez-Morocco) in coordination with Prof. Ali Ahaitouf as part of a CMCU PHC-Magreb project. Contact: ali_ahitouf@yahoo.fr
From January 2015 until now	<ul style="list-style-type: none"> ◆ Scientific collaboration with the Laboratory "Advanced Electronic Systems" of the University of Médéa- Algeria as part of Mr. Mohamed Maazouz's thesis on HEVC video coding. Contact: maazouz_m@yahoo.fr
May 2011- July 2015	<ul style="list-style-type: none"> ◆ Research internships in the Gaspard Monge LIGM computer laboratory, Marne la Vallée University - Paris in coordination with Mr. Thierry Grandpierre and Mr. Mohamed Akil.

PUBLISHED JOURNALS

Journal	Article	Information
	Abdessamad. El Ansari, Nejmeddine Bahri , Fatma Belghith, Anass Mansouri, Nouri Masmoudi and Ali Ahaitouf, Complexity Analysis and Fast algorithms Of the HEVC Encoder on DSP and ARM Processor, International Journal of Embedded Systems, 2019 (under review)	Submitted: July 2019 Indexed by: <u>Thomson reuters</u> Impact Factor:
	Nejmeddine Bahri , Randa Khemiri, Optimized HEVC Encoder Intra-Only Configuration on ARM-based Platform, IET Computers & Digital Technique, 2019 (under review)	Submitted: July 2019 Indexed by: <u>Thomson reuters</u> Impact Factor: 0.857



EL Ansari Abdessamad, **Nejmeddine Bahri**, Anass Mansouri, Nouri Masmoud, Ahaitouf Ali, Area & Power Efficient VLSI Architecture of Mode Decision in Integer Motion Estimation for HEVC Video Coding Standard, International Journal of Electrical and Computer Engineering (IJECE) Vol. 9, No. 4, **August 2019**, pp. 2469~2480, DOI: 10.11591/ijece.v9i4.pp2469-2480

Published on : Aug 2019
Indexed by: Scopus
Impact Factor: 0.37 (SJR 2018)



Nejmeddine Bahri & Walid Ouled Amor (2019) Intelligent power supply management of an autonomous hybrid energy generator, International Journal of Sustainable Engineering, DOI: 10.1080/19397038.2019.1581852

Published on : March 2019
Indexed by: Thomsons reuters



Mohamed Maazouz, Nouredine Batel, **Nejmeddine Bahri**, Nouri Masmoudi, Homogeneity-based fast CU partitioning algorithm for HEVC intra coding, Engineering Science and Technology, an International Journal, **2019**, ISSN 2215-0986, <https://doi.org/10.1016/j.jestch.2018.12.016>.

Published on : June 2019
Indexed by: Thomsons reuters
Impact Factor: 0.76 (SJR 2018)
Publisher: Elsevier



R. Khemiri, H. Kibeya, F. E. Sayadi, **N. Bahri**, M. Atri and N. Masmoudi, "Optimisation of HEVC motion estimation exploiting SAD and SSD GPU-based implementation," in IET Image Processing, vol. 12, no. 2, pp. 243-253, 2 **2018**. doi: 10.1049/iet-ipr.2017.0474

Published on : January 2018
Indexed by: Thomsons reuters
Impact Factor: 2.004 (SJR 2018)



Bahri, Nejmeddine & Grandpierre, Thierry & Ali Ben Ayed, Med & Masmoudi, Nouri & Akil, Mohamed. (2017). Embedded Real-Time H264/AVC High Definition Video Encoder on TI's KeyStone Multicore DSP. Journal of Signal Processing Systems. **2017**, Volume 86, Issue 1, pp 67–84. <https://doi.org/10.1007/s11265-015-1098-x>

Published on : January 2017
Indexed by: Thomsons reuters
Impact Factor: 1.035 (SJR 2018)
Publisher: Springer



Maazouz, M., Batel, N., **Bahri, N.**, Masmoudi, N., A DSP-Based Implementation of HEVC Encoder, (2016) International Review on Computers and Software (IRECOS), 11 (10), pp. 931-938. doi:<https://doi.org/10.15866/irecos.v11i10.10172>

Published on : January 2017
Indexed by: Scopus
Impact Factor: 0.336 (SJR 2016)
Publisher: Praise Worthy Prize



Nejmeddine Bahri, Nidhameddine Belhadj, Thierry Grandpierre, Mohamed Ali Ben Ayed, Nouri Masmoudi, Mohamed Akil: Real-time H264/AVC encoder based on enhanced frame level parallelism for smart multicore DSP camera. J. Real-Time Image Processing 12(4): 791-812 (2016)

Published on : Dec 2016
Indexed by: Thomsons reuters
Impact Factor: 2.588 (SJR 2018)
Publisher: Springer



Belhadj, Nidhameddine; **Bahri, Nejmeddine**; Marrakchi, Zied; Ben Ayed, Mohamed Ali; Masmoudi, Nouri; Mehrez, Habib: 'H.264/AVC high definition intra coding implementation on multiprocessor system on chip technology architecture', IET Computers & Digital Techniques, 2015, DOI: 10.1049/iet-cdt.2014.0151, IET Digital Library

Published on : Aug 2015
Indexed by: Thomsons reuters
Impact Factor: 0.857
Publisher: IET



Bahri, N., Werda, I., Grandpierre, T., Ben Ayed, M., Masmoudi, N., & Akil, M, "Optimizations for Real-Time Implementation of H264/AVC Video Encoder on DSP Processor," International Review on Computers & Software, 8 (9), 2013.

Published on : 2013
Indexed by: Scopus
Impact Factor: 0.336 (SJR 2016)
Publisher: IRECOS



Nejmeddine Bahri, Imen Werda, Amine Samet, Mohamed Ali Ben Ayed, Nouri Masmoudi," Fast Intra Mode Decision Algorithm for H264/AVC HD Baseline Profile Encoder," International Journal of Computer Applications (0975 – 8887) Volume 37– No.6, January 2012.

Published on : 2012
Publisher: IJCA

BOOK CHAPTERS

Journal

Chapter

Information



Randa Khemiri, **Nejmeddine Bahri**, Fatma Belghith, Soulef Bouaafia, Fatma Elzahra Sayadi, Mohamed Atri and Nouri Masmoudi, "Fast Motion Estimation's configuration using Diamond Search Pattern and (ECU, CFM, ESD) modes for reducing HEVC computational complexity", Digital imaging, IntechOpen, 2019 (**Accepted**)

Accepted : 2019
Publisher: IntechOpen



Nejmeddine Bahri, "encodeurs vidéo: implémentation temps réel sur des systèmes embarqués", ISBN-13: 978-3-8416-3746-8, Presses Académiques Francophones, Décembre 2015.

Accepted : Dec 2015
Publisher: Presses Académiques Francophones

PUBLISHED
COMMUNICATIONS

Conference	Article	Information
	<p>Nejmeddine Bahri, Mohamed Maazouz, Randa Khemiri, Nouri Masmoudi, Parallel implementation of HEVC encoder on multicore ARM-based platform, Systems, Signals & Devices (SSD 2019), Istanbul, 21-24 March 2019</p>	<p>Published on : 2019 Publisher: IEEE</p>
	<p>M. Mohamed, B. Nejmeddine, B. Noureddine, T. Abdelmoughni and M. Nouri, "Performance Evaluation of Frame-level Parallelization in HEVC Intra Coding Using Heterogeneous Multicore Platforms," 2018 International Conference on Applied Smart Systems (ICASS), Medea, Algeria, 2018, pp. 1-6. doi: 10.1109/ICASS.2018.8652076</p>	<p>Published on : Feb 2019 Publisher: IEEE</p>
	<p>N. Bahri, A. El Ansari, M. Maazouz, A. Ahaitouf and N. Masmoudi, "HEVC video encoder implementation on Texas Instruments platforms," 2016 17th International Conference on Sciences and Techniques of Automatic Control and Computer Engineering (STA), Sousse, 2016, pp. 165-170. doi: 10.1109/STA.2016.7952073</p>	<p>Published on : June 2017 Publisher: IEEE</p>
	<p>H. Kibeya, N. Bahri, M. A. Ben Ayed and N. Masmoudi, "SAD and SSE implementation for HEVC encoder on DSP TMS320C6678," 2016 International Image Processing, Applications and Systems (IPAS), Hammamet, 2016, pp. 1-6. doi: 10.1109/IPAS.2016.7880116</p>	<p>Published on : March 2017 Publisher: IEEE</p>
	<p>R. Khemiri, N. Bahri, F. Belghith, F. E. Sayadi, M. Atri and N. Masmoudi, "Fast motion estimation for HEVC video coding," 2016 International Image Processing, Applications and Systems (IPAS), Hammamet, 2016, pp. 1-4. doi: 10.1109/IPAS.2016.7880120</p>	<p>Published on : March 2017 Publisher: IEEE</p>
	<p>M. Maazouz, N. Bahri, N. Batel, A. Toubal and N. Masmoudi, "Parallel implementation of Kvazaar HEVC on multicore ARM processor," 2016 8th International Conference on Modelling, Identification and Control (ICMIC), Algiers, 2016, pp. 1086-1091. doi: 10.1109/ICMIC.2016.7804275</p>	<p>Published on : January 2017 Publisher: IEEE</p>
	<p>M. Maazouz, S. T. Kebir, B. Bengherbia, A. Toubal, N. Batel and N. Bahri, "A DCT-based algorithm for multi-channel near-lossless EEG compression," 2015 4th International Conference on Electrical Engineering (ICEE), Boumerdes, 2015, pp. 1-5. doi: 10.1109/INTEE.2015.7416805</p>	<p>Published on : Feb 2016 Publisher: IEEE</p>
	<p>N. Bahri, N. Belhadj, M. A. Ben Ayed, N. Masmoudi, T. Grandpierre and M. Akil, "Real-time H264/AVC high definition video encoder on a multicore DSP TMS320C6678," International Conference on Computer Vision and Image Analysis Applications (ICCVIA), Sousse, 2015, pp. 1-6. doi: 10.1109/ICCVIA.2015.7351893</p>	<p>Published on : Dec 2015 Publisher: IEEE</p>



Bahri, N.; Grandpierre, T.; Ben Ayed, M.A.; Masmoudi, N.; Akil, M., "GOP level parallelism implementation for real-time H264/AVC video encoder on multicore DSP TMS320C6472," Education and Research Conference (EDERC), 2014 6th European Embedded Design in , vol., no., pp.152,156, 11-12 Sept. **2014**.

Published on : 2014
Publisher: IEEE



N. Belhadj, **N. Bahri**, Z. Marrakchi, M. A. Ben Ayed, N. Masmoudi and H. Mehrez, "H.264/AVC intra prediction encoding chain implementation on MPSoC based on slice level parallelism," *2014 15th International Conference on Sciences and Techniques of Automatic Control and Computer Engineering (STA)*, Hammamet, **2014**, pp. 267-272. doi: 10.1109/STA.2014.7086797

Published on : 2014
Publisher: IEEE



A. Kessentini, **N. Bahri**, N. Masmoudi, A. Samet and M. A. Ben Ayed, "DSP-based down-sampling process using lanczos filter bank," *2014 1st International Conference on Advanced Technologies for Signal and Image Processing (ATSIP)*, Sousse, **2014**, pp. 148-152. doi: 10.1109/ATSIP.2014.6834595

Published on : 2014
Publisher: IEEE



Nejmeddine Bahri, Thierry Grandpierre, Nouri Masmoudi, Mohamed Akil, "Optimisations structurelles et matérielles de l'encodeur vidéo H264/AVC sur un seul cœur d'un DSP multicœurs TMS320C6472, " Gretsi2013, Brest september **2013**

Published on : 2013
Publisher: IEEE



Belhadj, N.; **Bahri, N.**; Ben Ayed, M.A.; Marrakchi, Z.; Mehrez, H., "Data level parallelism for H264/AVC baseline intra-prediction chain on MPSoC," *Systems, Signals & Devices (SSD)*, 2013 10th International Multi-Conference on , vol., no., pp.1,4, 18-21 March **2013**

Published on : 2013
Publisher: IEEE



Bahri, N.; Masmoudi, N.; Werda, I.; Samet, A.; Ben Ayed, M.A., "Fast intra mode decision algorithm based on inter prediction mode for H264/AVC," *Electrotechnical Conference (MELECON)*, 2012 16th IEEE Mediterranean , vol., no., pp.848,851, 25-28 March **2012**

Published on : 2012
Publisher: IEEE

OPENING ON THE ENVIRONMENT

N°	Activities
1	Member of the Tunisian Red Crescent 2018-2019
2	Member of the Junior international chamber (JCI) 2016-2018
3	President of the Robotics Club FSM-ROBOTECH 2016-2020
4	Member of the SETIT scientific committee 2016 and 2018

ANNEX